

INFORMATION DISCLOSURE CITATION

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PTO-1449

Complete If Known

Application Number	10/662,204
Filing Date	September 12, 2003
First Named Inventor	Nguyen et al.
Art Unit	2819
Examiner Name	Cho, James Hyonchol
Attorney Docket Number	60809-0127-US

Sheet 1 of 2

U.S. PATENT DOCUMENTS

Examiner Initials	Document Number Number - Kind Code ¹	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Class	Subclass	Filing Date if Appropriate
<i>jc</i>	5,182,467	01/1993	Taylor et al.	327	175	
	5,194,765	03/1993	Dunlop et al.	326	87	
	5,254,883	10/1993	Horowitz et al.	326	30	
	5,373,477	12/1994	Sugibayashi, Tadahiko	327	541	
	5,430,400	07/1995	Herlein	327	108	
	5,530,377	06/1996	Walls	326	30	
	5,783,956	07/1998	Ooishi, Tsukasa	327	143	
	5,821,808	10/1998	Fujima, Shiro	327	538	
	5,847,578	12/1998	Noakes et al.	326	39	
	5,933,051	08/1999	Tsuchida et al.	323	315	
	5,949,254	09/1999	Keeth	326	87	
	5,959,481	09/1999	Donnelly et al.	327	170	
	6,047,346	04/2000	Lau et al.	710	126	
	6,163,178	12/2000	Stark et al.	327	108	
	6,222,354	04/2001	Song, Ki-Whan	323	273	
	6,342,800	01/2002	Stark et al.	327	170	
	6,618,786	09/2003	Sidiropoulos et al.	710	305	
<i>jc</i>	6,657,468	12/2003	Best et al.	327	170	
	6,661,268	12/2003	Stark et al.	327	170	

FOREIGN PATENT DOCUMENTS

Examiner Initials	Foreign Patent Document Country Code ² - Number ³ - Kind Code ⁴ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Class	Subclass	Translation	
						Yes	No
<i>jc</i>	EP 0 463 316 A1	01/02/1992		H04L	12/40	<input checked="" type="checkbox"/>	
<i>jc</i>	EP 0 482 392 A2	04/29/1992		H04L	25/08	<input checked="" type="checkbox"/>	
<i>jc</i>	JP 58-54412 (A)	03/31/1983		G05F1	56	<input checked="" type="checkbox"/>	<i>Abstract only</i>

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published
	Allen, Arnold O., "Probability, Statistics, and Queuing Theory with Computer Science Applications", 2nd Edition, CH 7, pp 450, 458-459
<i>jc</i>	Chappell, Terri I. et al., "A 2ns Cycle, 4ns Access 512 kb CMOS ECL SRAM", <i>IEEE International Solid State Circuits Conference</i> , 1991, pp 50-51 <i>No month</i>
<i>jc</i>	Donnelly, Kevin S. et al., "A 660 MB/s Interface Megacell Portable Circuit in 0.3 μ m-0.7 μ m CMOS ASIC", <i>IEEE Journal of Solid State Circuits</i> , 31(12):1995-2003 (1996) <i>No month</i>

Examiner Signature	<i>James Cho</i>	Date Considered	<i>8/21-5</i>
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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Sheet	2	of	2		
<input checked="" type="checkbox"/>	Pilo, Harold et al., "A 300 MHz 3.3V 1 Mb SRAM Fabricated in a 0.5 μ m CMOS Process", <i>IEEE International Solid State Circuits Conference</i> , pp. 148-149 (1996) <i>No month</i>				
<input type="checkbox"/>	Schumacher, Hans-Jürgen et al., "CMOS Subnanosecond True-ECL Output Buffer", <i>IEEE Journal of Solid-State Circuits</i> , 25(1):150-154 (1990) <i>No month</i>				
<input type="checkbox"/>	Sidiropoulos, Stefanos et al., "A 700-Mb/s/pin CMOS Signaling Interface Using Current Integrating Receivers", <i>IEEE Journal of Solid-State Circuits</i> , 32(5):681-690 (1997) <i>No month</i>				
<input type="checkbox"/>	Yang, Tsen-Shau et al., "A 4-ns 4Kx1-bit Two-Port BiCMOS SRAM", <i>IEEE Journal of D-State Circuits</i> , 23(5):1030-1040 (1988) <i>No month</i>				
<input checked="" type="checkbox"/>	EIA/JDEC, JC-42.3 Committee on RAM Memories, Minutes of Meeting 63, July 21, 1992, Denver, CO.				
<input checked="" type="checkbox"/>	EIA/JDEC, JC-42.3 Committee on RAM Memories, Minutes of Meeting 64, Sept. 16-17, 1992, Crystal City, VA.				

Examiner Signature	<i>James Cho</i>	Date Considered	<i>8/2/05</i>
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